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IMPORTANT NOTICE TELECOPY/FACSIMILE COVER LETTER

TO: Examiner J. Fenty
Art Unit 2815
U.S. Patent and Trademark Office

DATE: 5/28/2002

FROM: Michael L. Crapenhoft

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MESSAGE:

Amendment and Petition for Extension of Time (with duplicate) attached.

U.S. Patent Application Serial No. 09/803,624

FOR INTERNAL PURPOSES ONLY

TELECOPY/FAX NUMBER: 1-703-872-9318

CLIENT NUMBER: 81738.0292

ATTORNEY BILLING NUMBER: 3118

CONFIRMATION NUMBER: 1-213-337-6711

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5/31/02
Mollish

PATENT
81738.0292

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

KOJIMA, et al.

Serial No: 09/803,624

Filed: March 9, 2001

For: SMALL GEOMETRY HIGH
VOLTAGE SEMICONDUCTOR
DEVICE

Art Unit: 2815

Examiner: FENTY, J.

I hereby certify that this correspondence
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May 28, 2002
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Michael Cragenhoft Reg. No. 37,115

Name Mike Cragenhoft May 28, 2002

Signature Date

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated January 31, 2002, the time for response to which is extended by one month by the accompanying petition from April 30, 2002 to May 30, 2002, please amend the above-referenced application as follows:

IN THE CLAIMS:

✓ COPY RECEIVED

Please cancel claim 26 without prejudice.

MAY 28 2002

Please replace the text of claims 27 and 28 with the following text:

TECHNOLOGY CENTER 2800

B1

27. (Amended) A semiconductor integrated circuit according to claim 28, wherein the minimum gate length in the channel length direction of the LVMISFET is in the range of 1.5 - 2.5 μ m.